CLOCK OPTIONS:

Crystal: Omit all but crystal and 2 39pF caps

SMD OSC: Omit crystal and 39pF on CLK line. Put 1nF from /CLK to GND.

The 220 and 68 ohm resistors are for a 3.3V clock. Change as required for other amplitudes.

EXT OSC: Omit SMD_OSC (or disable from pin 11), 39pF from CLK to GND, put 1nF from /CLK to GND, adjust 220 and 68 for proper amplitude. Omit crystal.

AVccD

DVccD

DDS Chip Power Bypass Caps